

04/22/2008

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicants:</b>	Wing K Luk, et al.	<b>Examiner:</b>	Bernstein, Allison
<b>Serial No.:</b>	10/735,061	<b>Group:</b>	Art Unit 2824
<b>Filed:</b>	December 11, 2003	<b>Docket:</b>	YOR920030136US1 (8728-621)
<b>For:</b>	<b>GATED DIODE MEMORY CELLS</b>		

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Commissioner for Patents  
P.O. Box 1450  
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**RESPONSE UNDER 37 C.F.R. 1.116**

Examiner:

This reply is in response to the Office Action dated February 20, 2008. Please consider the following remarks.